



Ευρωπαϊκή Ένωση  
Ευρωπαϊκό Ταμείο  
Περιφερειακής Ανάπτυξης

ΕΛΛΗΝΙΚΗ ΔΗΜΟΚΡΑΤΙΑ  
ΥΠΟΥΡΓΕΙΟ  
ΟΙΚΟΝΟΜΙΑΣ & ΑΝΑΠΤΥΞΗΣ  
ΕΙΔΙΚΗ ΓΡΑΜΜΑΤΕΙΑ ΕΠΠΑ & ΤΣ  
ΕΙΔΙΚΗ ΥΠΗΡΕΣΙΑ ΔΙΑΧΕΙΡΙΣΗΣ ΕΠΑΝΕΚ

Με τη συγχρηματοδότηση της Ελλάδας και της Ευρωπαϊκής Ένωσης

ΕΠΑΝΕΚ 2014-2020  
ΕΠΙΧΕΙΡΗΣΙΑΚΟ ΠΡΟΓΡΑΜΜΑ  
ΑΝΤΑΓΩΝΙΣΤΙΚΟΤΗΤΑ  
ΕΠΙΧΕΙΡΗΜΑΤΙΚΟΤΗΤΑ  
ΚΑΙΝΟΤΟΜΙΑ

ΕΣΠΑ  
2014-2020  
ανάπτυξη - εργασία - αλληλεγγύη



## Δράση ΕΡΕΥΝΩ – ΔΗΜΙΟΥΡΓΩ – ΚΑΙΝΟΤΟΜΩ

Συγχρηματοδότηση από την Ευρωπαϊκή Ένωση και τους εθνικούς πόρους μέσω του Ε.Π. Ανταγωνιστικότητα, Επιχειρηματικότητα & Καινοτομία (ΕΠΑνΕΚ)

# RADAR

“Ετερογενής Τρισδιάστατη Ολοκλήρωση με χρήση ρηξικέλευθων νανοτεχνολογιών για τη νέα γενιά μικροκυματικών πομποδεκτών ισχύος”

Κωδικός έργου: T1ΕΔΚ-00329

## Ενότητα Εργασίας 3: ΤΕΧΝΟΛΟΓΙΚΑ ΠΡΩΤΟΚΟΛΛΑ

### Παραδοτέο 3.6 - M12

### Ετήσιες δράσεις διάχυσης της ΕΕ3

Ημερομηνία : M12

Επικεφαλής παραδοτέου: ΙΤΕ

Συμμετέχοντες

ΙΤΕ: Γ. Κωνσταντινίδης

ΠΡΙΣΜΑ ΑΕ: Σ. Συμεωνίδης, Χ. Σπανδωνίδης, Ι. Σιούτας, Δ. Ξάφη, Χ. Ρομπίδου, Ν. Θεμελής, Φ. Γιαννόπουλος

GLONATECH: Β. Βαμβακάς

INN: Π. Δημητράκης, Pascal Normand, Βασίλειος Ιωάννου-Σουγλερίδης

ΕΚΠΑ: Γ. Παπαϊωάννου, Δ. Μπιρμπιλιώτης, Μ. Κουτσουρέλη, Γ. Θεοχάρης

ΣΝΔ: Ε. Καραγιάννη, Ν. Μελανίτης, Χ. Βαζούρας, Ν. Φαφαλιός

ΑΜΕΝ: Δ. Νιάρχος, Κ. Καρυοφύλλης



1. Αντικείμενο της Αναφοράς .....	2
2. Ιστότοπος “RADAR” .....	2
2.1. ΕΙΣΑΓΩΓΗ.....	2
2.2. ΣΧΕΔΙΑΣΜΟΣ, ΣΥΝΤΗΡΗΣΗ ΚΑΙ ΕΝΗΜΕΡΩΣΗ .....	2
2.3. ΔΙΑΦΘΡΩΣΗ ΙΣΤΟΣΕΛΙΔΑΣ .....	3
3. Δημοσιεύσεις σε διεθνή περιοδικά και συνέδρια (acknowledging RADAR) .....	5
3.1. Διεθνή Περιοδικά .....	5
3.2. Διεθνή Συνέδρια .....	7
4. Παρουσιάσεις σε διεθνείς επιστημονικές συναντήσεις .....	9
5. Προβολή σε Μέσα Κοινωνικής Δικτύωσης και Εταιρικές Ιστοσελίδες.....	10

## 1. Αντικείμενο της Αναφοράς

Αντικείμενο της παρούσας αναφοράς είναι η καταγραφή των ενεργειών που έχουν γίνει κατά τη διάρκεια του πρώτου έτους του προγράμματος για τη διάχυση των αποτελεσμάτων της έρευνας στα πλαίσια της ΕΕ3 του PANTAP. Είναι αναμενόμενο η ποσότητα των ενεργειών αυτών να είναι σχετικά περιορισμένη για τους πρώτους 12 μήνες.

Αρχικά δημιουργήθηκε το ΙΤΕ ιστότοπος που είναι συνεχώς ενημερωμένος. Ο ιστότοπος παρουσιάζει τα προφίλ των εταιρών της κοινοπραξίας και τις δράσεις διάχυσης. Η τακτική προώθηση των ειδήσεων και των εκδηλώσεων του έργου γίνεται μέσω αυτού που θα παραμείνει ενεργός για τουλάχιστον τέσσερα ακόμη χρόνια.

Η κοινοπραξία φροντίζει τα αποτελέσματα να δημοσιεύονται σε περιοδικά μεγάλου αντικτύπου και να ανακοινώνονται σε σημαντικά διεθνή συνέδρια.

Επίσης, στόχος της κοινοπραξίας είναι η διάδοση των επιτευγμάτων του έργου σε διεθνή τεχνικά φόρουμ και σε ημερίδες στα πλαίσια των δράσεων της εθνικής υποδομής Innovation-EL και της ελληνικής επιστημονικής εταιρείας Micro & Nano. Επιπλέον θα γίνουν προσπάθειες για διάχυση προς τις σχετικές βιομηχανικές και ακαδημαϊκές οργανώσεις Public-Private Partnership (PPP clusters) της ΕΕ και άλλους φορείς όπως η European Defense Agency (EDA).

Η κοινοπραξία προτίθεται να οργανώσει θεματική ημερίδα για να διαφημίσει τα τεχνικά και επιστημονικά επιτεύγματα του έργου.

Οι δραστηριότητες προβολής του έργου και διάχυσης των αποτελεσμάτων του έργου περιλαμβάνουν και συμμετοχή σε εκδηλώσεις "ανοικτής ημέρας", παρουσιάσεις και ομιλίες κατά τη διάρκεια της "Βραδιάς του Ευρωπαίου Ερευνητή" και της "Μάθε περισσότερα, γίνε καλύτερος" εκπαιδευτικές δράσεις αφιερωμένες στα σχολεία. Φυλλάδια και δελτία θα παρουσιάζουν εκλαϊκευμένα τα αποτελέσματα του έργου στο ευρύ κοινό.

## 2. Ιστότοπος "RADAR"

### 2.1. ΕΙΣΑΓΩΓΗ

Ο ιστότοπος "RADAR" σχεδιάστηκε για να παρέχει πληροφορίες σχετικά με το έργο και για το πώς θα επιτευχθούν οι στόχοι του. Ο ιστότοπος του έργου "RADAR" διατίθεται στο <https://radar-project.iesl.forth.gr/> και είναι προσβάσιμο από κινητές συσκευές. Ο ιστότοπος σχεδιάστηκε για να αναθεωρεί και να αναπτύσσει συνεχώς τις δραστηριότητες διάδοσης στο πλαίσιο του έργου και να επιτρέπει συνεχή βελτίωση.

Ο δικτυακός τόπος περιλαμβάνει δύο διαφορετικούς τομείς, αφενός, έναν ιδιωτικό χώρο, ο οποίος χρησιμοποιείται κυρίως ως χώρος αποθήκευσης εγγράφων και ανταλλαγής διαβαθμισμένων πληροφοριών και, αφετέρου, δημόσιος χώρος. Ο δημόσιος χώρος του ιστότοπου θα προωθήσει το έργο "RADAR" μέσω σχετικών πληροφοριών, συμπεριλαμβανομένων των στόχων του έργου, του ιστορικού εταίρου και των μη ταξινομημένων αποτελεσμάτων του έργου.

Όλοι οι εταίροι θα συμμετάσχουν συλλογικά στην επικαιροποίηση διάδοσης της ιστοσελίδας παρέχοντας ενημερωμένες πληροφορίες

### 2.2. ΣΧΕΔΙΑΣΜΟΣ, ΣΥΝΤΗΡΗΣΗ ΚΑΙ ΕΝΗΜΕΡΩΣΗ

Ο ιστότοπος σχεδιάστηκε με έναν εύκολο τρόπο πλοήγησης, ο οποίος επιτρέπει στους χρήστες του ιστότοπου να μάθουν για το έργο και τα αποτελέσματά του, ειδήσεις κλπ. Όλα τα τμήματα του ιστότοπου έχουν στην κορυφή το λογότυπο RADAR (στα αριστερά) και μια αναφορά στη χρηματοδότηση εργαλεία (στα δεξιά).

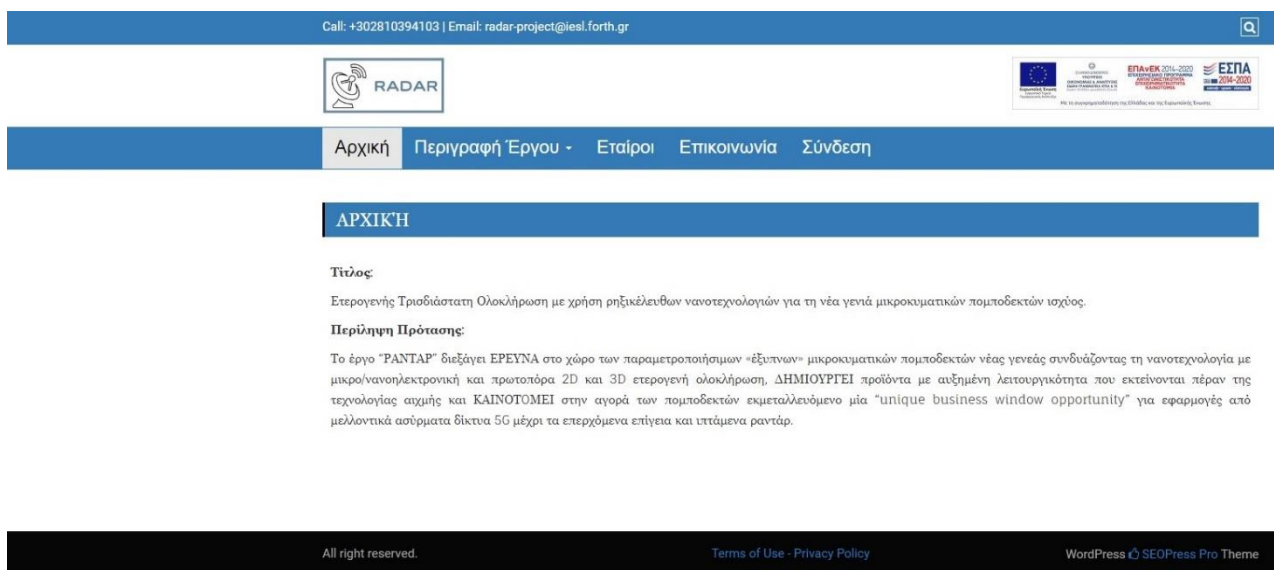


Η ιστοσελίδα φιλοξενείται και διοικείται από την Ομάδα Μικροηλεκτρονικής Έρευνας (MRG) του Ινστιτούτου Ηλεκτρονικής Δομής και Λείζερ (IESL) του Ιδρύματος Τεχνολογίας και Έρευνας (ΙΤΕ). Ένα αφοσιωμένο μέλος του προσωπικού της MRG επικαιροποιεί τακτικά τον ιστότοπο, μοιράζεται ειδήσεις, πληροφορίες σχετικά με εκδηλώσεις του έργου, παρουσιάσεις και άλλα σχετικά με το υλικό του έργου.

Ο ιστότοπος κατασκευάστηκε χρησιμοποιώντας το WordPress. Το WordPress είναι ένα ελεύθερο και ανοικτού κώδικα σύστημα διαχείρισης περιεχομένου (CMS) βασισμένο σε PHP και MySQL. Μεταξύ άλλων πλατφορμών, το Wordpress είναι μια δημοφιλής, καλά υποστηριζόμενη πλατφόρμα δημοσίευσης blogging και έχει μια μεγάλη κοινότητα που θα ελαχιστοποιήσει το ποσό της πολύπλοκης προσαρμογής που θα χρειαστεί να κάνει ο Συνεργάτης του Έργου.

### 2.3. ΔΙΑΡΘΡΩΣΗ ΙΣΤΟΣΕΛΙΔΑΣ

Ο δημόσιος χώρος έχει σχεδιαστεί για να εξασφαλίσει τη δημόσια προβολή των επιτευγμάτων και των αποτελεσμάτων του RADAR. Ο δημόσιος χώρος περιλαμβάνει πέντε κύριες σελίδες, όπως φαίνεται στο Σχήμα 1: Αρχική σελίδα, περιγραφή της εργασίας, συνεργάτες, διάδοση, επικοινωνία και μια καρτέλα "Σύνδεση" για να έχουν πρόσβαση οι συνεργάτες στην ιδιωτική περιοχή (προστατεύεται με κωδικό πρόσβασης). Αγγλικά είναι η γλώσσα που επιλέξατε.

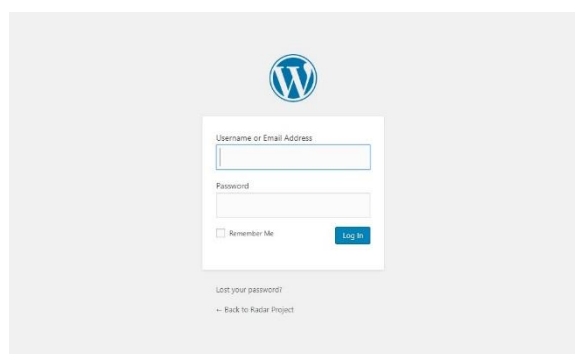


Σχήμα 1: Στιγμιότυπο οθόνης της αρχικής σελίδας του ιστότοπου "RADAR"

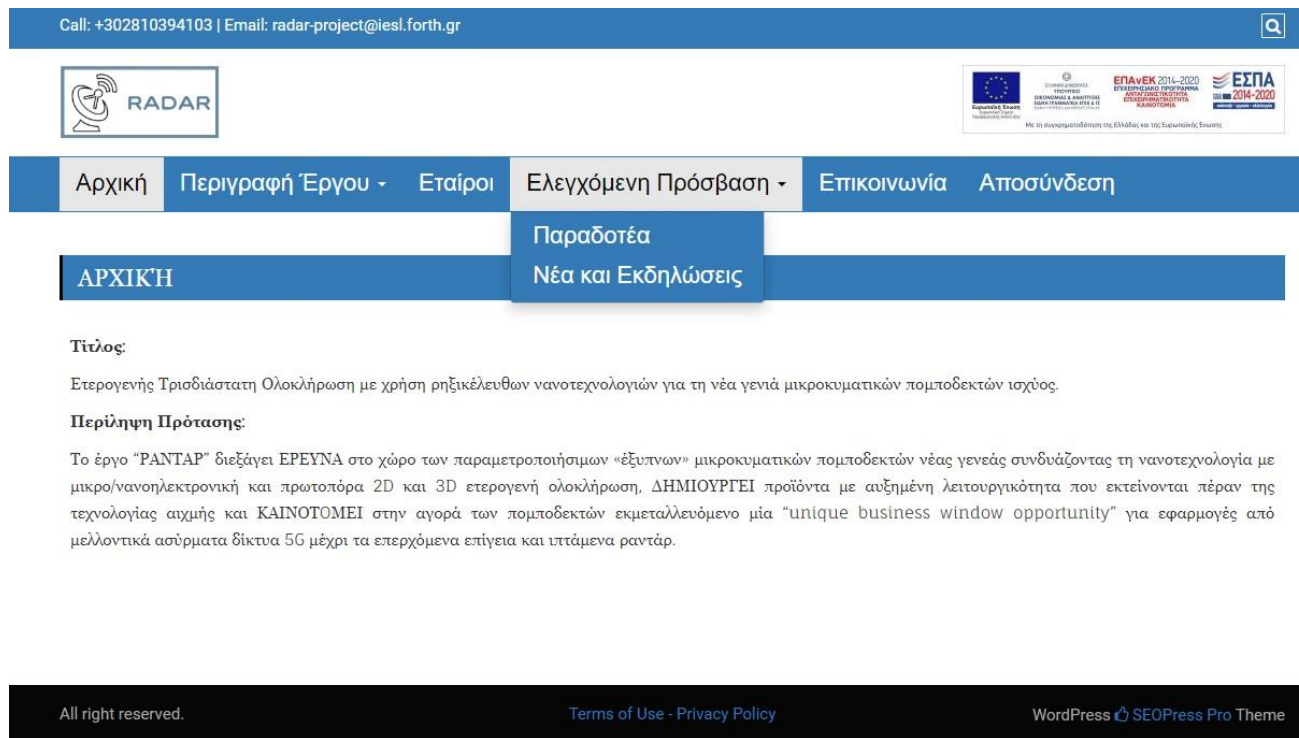
- Η σελίδα «**Αρχική**» παρέχει γενικές πληροφορίες για το έργο «RADAR».
- Η σελίδα «**Περιγραφή Έργου**» παρουσιάζει το αντικείμενο, τους στόχους και την μεθοδολογία υλοποίησης του έργου.
- Η σελίδα «**Εταίροι**» παρουσιάζει τις επωνυμίες των εταίρων που συμμετέχουν στο έργο, καθώς και τις συντομογραφίες μαζί με τις περιφέρειες του κάθε φορέα.
- Η σελίδα «**Επικοινωνία**» παρουσιάζει πληροφορίες (τηλέφωνα και διεύθυνση επικοινωνίας) που σχετίζονται με τον συντονιστή του προγράμματος.
- Η σελίδα «**Σύνδεση**» δίνει την δυνατότητα πρόσβασης στην ιδιωτική περιοχή του έργου «RADAR». Οι εταίροι ανακατευθύνονται στη σελίδα σύνδεσης και ζητούν τη σύνδεσή τους και τον κωδικό πρόσβασής τους για να μουν στην περιοχή.



Ο ιδιωτικός χώρος έχει σχεδιαστεί ως μια ενιαία πλατφόρμα εργασίας μόνο για εξουσιοδοτημένους χρήστες μόνο. Ο χώρος αυτός χρησιμοποιείται για την κοινή χρήση όλων των εμπιστευτικών εγγράφων και η πρόσβαση σε αυτήν απαιτεί διαδικασία σύνδεσης όπου όλοι οι εταίροι έχουν λάβει ο καθένας ξεχωριστά από ένα αναγνωριστικό ονόματος και κωδικό πρόσβασης. Στα παρακάτω Σχήματα 2 και 3 είναι στιγμιότυπα οθόνης της ιστοσελίδας του Έργου «RADAR».



**Σχήμα 2:** Για να έχει κάποιος χρήστη/εταίρος πρόσβαση στην ελεγχόμενη περιοχή της ιστοσελίδας, θα πρέπει να κάνει σύνδεση με χρήση ονόματος και κωδικού που του έχει δοθεί από τον διαχειριστή της ιστοσελίδας.



**Σχήμα 3:** Ο χρήστης/εταίρος της ιστοσελίδας όταν συνδεθεί επιτυχώς στην ελεγχόμενη περιοχή έχει πρόσβαση στις κατηγορίες «Παραδοτέα» και «Νέα και Εκδηλώσεις»

### 3. Δημοσιεύσεις σε διεθνή περιοδικά και συνέδρια (acknowledging RADAR)

#### 3.1. Διεθνή Περιοδικά

1. D. Birbiliotis, G. Stavrinidis, M. Koutsourelis, G. Konstantinidis, G. Papaioannou, A. Ziaei, "A comparative study of nanostructured Silicon-Nitride electrical properties for potential application in RF-MEMS capacitive switches", ELSEVIER, Microelectronics Reliability, 100-101, 2019



#### A comparative study of nanostructured Silicon-Nitride electrical properties for potential application in RF-MEMS capacitive switches

D. Birbiliotis<sup>a,\*</sup>, G. Stavrinidis<sup>a</sup>, M. Koutsourelis<sup>a</sup>, G. Konstantinidis<sup>a</sup>, G. Papaioannou<sup>a</sup>, A. Ziaei<sup>b</sup>

<sup>a</sup>Physics Department, Solid State Physics Section, University of Athens, Athens 15704, Greece

<sup>b</sup>Institute of Electronic Structure and Laser (IESL), Foundation for Research and Technology - Hellas (FOYTE), Heraklion 70013, Greece

<sup>c</sup>Radio Research and Technology, FORTH/ICEPP, Heraklion, Greece

#### ABSTRACT

The present paper aims to investigate the electrical properties of nanostructured SiNx with embedded (i) CNTs of random orientation, (ii) columnar bunches of vertically aligned CNTs and (iii) columnar Au nanorods. MIM capacitors were used to assess the effect of dielectric film structure on the electrical properties. The charge transport mechanism was assessed through current - voltage characterization and the charge draining efficiency through the top electrode potential decay. In the case of random oriented CNTs, the transport mechanisms are Frenkel-Poole and field emission at low and high electric fields respectively. In the case of Au nanorods, the hopping and Frenkel-Poole mechanisms are detected at low and high electric fields respectively. Finally, for vertically aligned CNTs the current-voltage characteristics is found to follow ohm's law. The discharge rate through the bulk material is found to depend on nanofiber morphology.

#### 1. Introduction

RF-MEMS capacitive switches are promising devices for several applications, especially in the field of wireless communications. Their ultra-high linearity, almost zero power consumption, compatibility with silicon technology and the ability to manage signals close to hundreds GHz, make them the most prominent candidates to succeed the conventional semiconductor based switches [1]. In contrary of these attractive benefits, there are reliability issues, among them the most severe is dielectric charging, hindering their commercialization as "component off-the-shelf". During the devices' operation, specifically during actuation, charges are injected and trapped inside the dielectric causing erratic device behavior, which in most cases may lead to stiction and device failure.

In order to mitigate the dielectric charging significant effort has been paid employing various composition SiNx [2-4]. In all cases the dielectric film has been extensively studied aiming to control its electrical properties considering that beyond the percolation threshold,  $x_c \approx 1.0$ , the Si-Si bonds fail to form continuous percolation paths across the network [5]. The intensive study of Si-rich material [6-8] led to better understanding of SiNx electrical properties but still did not provide solution to the persisting problem of dielectric charging.

In a MEMS capacitive switch during up-state the stored charge can only be drained through the bottom electrode, so the ability to quickly remove the injected and trapped charge is essential. In order to enhance this process the first devices with nanostructured dielectric film, with

carbon nanotubes (CNTs) embedded in the upper part of the SiNx film was presented by C. Boidas et al. in [9] where they demonstrated that the presence of CNTs improves the device reliability. Considering the effect of percolation and the variable length tunneling taking place in nanostructured dielectrics, their impact on charging mitigation has been discussed in [10].

The present paper aims to investigate, for the first time, the electrical properties of nanostructured SiNx that has been fabricated with different techniques, by embedding (i) CNTs with random orientations, (ii) columnar bunches of aligned CNTs and (iii) columnar Au nanorods. In all cases, the dielectric film has the same thickness and the deposition was performed in two steps of equal thickness where the bottom film was nanostructured. Metal-Insulator-Metal (MIM) capacitors and MEMS capacitive switches were used to assess the effect of dielectric film structure on the electrical properties. The charge transport mechanism was assessed through current - voltage characteristics and the charge draining efficiency through the top electrode potential decay.

#### 2. Experimental data

As already mentioned, MIM capacitors with three different nanostructured dielectrics were fabricated in order to assess dependence of the electrical properties on the material structure. The details of nanostructured dielectric films fabrication is presented below.

D. Birbiliotis, et al.

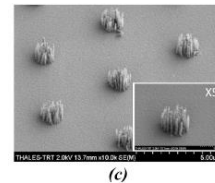
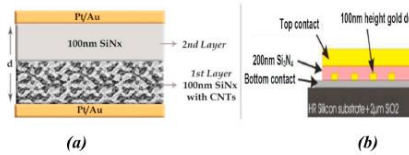


Fig. 1. (a) Schematic of MIM capacitor with randomly oriented CNTs, (b) Schematic of MIM capacitor with Au nanorods, (c) SEM photo of the bunches of vertically aligned CNTs.

#### 2.1. Randomly oriented CNTs

The first group of devices consisted of MIM capacitors with SiNx with embedded CNTs in random orientations (Fig. 1a). The MIM capacitors have symmetrical metal contacts (Au/Pt/dielectric film/Pt/Au) in order to avoid insulator-contacts work function differences and contact area of 1 mm diameter. The dielectric film total thickness was 200 nm SiNx with embedded CNTs in the lower 100 nm film, detailed description presented in [11].

#### 2.2. Vertically aligned CNTs

The devices of this group were fabricated on High-Resistivity (HR) silicon substrates. After the bottom contact deposition, TiN barrier pads were patterned by e-beam lithography, in order to prevent the diffusion of Ni, which was deposited after this step. The CNTs growth was carried out by the CNT CVD equipment - Black Magic by Axison, where the feeding gases were chosen to be  $\text{NH}_3$  and  $\text{C}_2\text{H}_2$  [12]. The growth of CNTs lasted 15 min at 650 °C with 650 V plasma voltage. The next step was the consecutive deposition of SiNx of total thickness of 200 nm taking place in two steps, where the process stopped for the removal of uncovered CNTs edges by plasma etching. The produced CNTs bunches had 100 nm height, the bunch diameter of 500 nm and 5 μm spacing between them which provide a direct comparison with the perspective samples of Au nanorods with the same dimensions (Fig. 1c). The MIM capacitors used had an area of 480x480 μm<sup>2</sup>.

#### 2.3. Au nanorods

The last group of dielectric material used in present work was

fabricated in practically three steps. A 100 nm SiNx layer was deposited with HF (13.56 MHz) PECVD method at 200 °C on bottom contact (CPW line for MEMS switches), which was deposited on SiO<sub>2</sub>/high resistivity Si substrate. Holes with diameter of 500 nm were opened in the SiNx film and 100 nm Au nanorods were grown directly on the bottom contact. Finally, the nanostructured dielectric was covered with 100 nm PECVD SiNx (HF PECVD at 200 °C). The nanorods spacing was 5 μm and the capacitor area was also 480x480 μm<sup>2</sup>, shown in Fig. 1b.

The MIM capacitors were assessed with current-voltage (I-V) characteristics in vacuum at 300 K with the aid of a Keithley 6487 source-meter/electrometer. The top electrode applied voltage was always positive to ensure electron injection from bottom electrode through the nanostructured film. The SiNx composition  $x = \frac{a}{b}$  for both CNTs bunches and Au nanorods was estimated to  $x = 1.25$  while the randomly oriented CNTs have SiNx composition was found  $x = 1.22$  [13].

The discharging process through the bulk material has been investigated with the aid of a single-point Kelvin Probe system [14], at room temperature and at ambient conditions. A polarization field with intensity of 1 MV/cm has been applied for 20 min and the discharging process was monitored for 10<sup>5</sup> s. The charging conditions for MEMS with randomly oriented CNTs and Au nanorods were set to 20 MV/cm for 40 min and 1.5 MV/cm for 5 min respectively. The discharge process in MEMS capacitive switches has been monitored through the shift of up-state C-V characteristic towards pre-stress equilibrium.

#### 3. Theoretical background

The introduction of nanostructured dielectric films in MEMS capacitive switches, although shown to provide better device performance

\* Corresponding author.

E-mail address: dimbil@phys.uoa.gr (D. Birbiliotis).

<https://doi.org/10.1016/j.micrel.2019.06.052>

Received 15 May 2019; Received in revised form 12 June 2019; Accepted 21 June 2019

Available online 23 September 2019

0026-2714/© 2019 Elsevier Ltd. All rights reserved.

Με τη συγχρηματοδότηση της Ελλάδας και της Ευρωπαϊκής Ένωσης

D. Bampetis, et al.

[9], has not been studied in depth. As already mentioned, the increase of charge draining through percolation has been discussed in [10] and initial assessment results of field emission in MEMS dielectric films with randomly oriented CNTs were demonstrated in [11,15].

In a nanostructured dielectric the measured current has to consider the basic charge transport mechanisms contributing through parallel paths [10]:

$$J = J_1 + J_2 + J_3 \quad (1)$$

$$J_1 = C_1 F \exp\left(-\frac{q\phi_1}{kT}\right) \exp\left(\frac{qF}{kT}\right) \exp\left(-\frac{E_a}{kT}\right) \quad (2)$$

$$J_2 = C_2 F \exp\left(-\frac{q\phi_2}{kT}\right) \exp\left(\frac{qF}{kT}\right) \exp\left(-\frac{E_a}{kT}\right) \quad (3)$$

$$J_3 = C_3 F^2 \exp\left(-\frac{E_a}{kT}\right) \quad (4)$$

In this model  $J_1$  is the hopping mechanism current,  $J_2$  the contribution of Poole-Frenkel conduction and  $J_3$  the Fowler-Nordheim (F-N) tunneling current, where  $C_1, C_2, C_3$  are constants,  $q\phi_1, q\phi_2$  the barrier height for each mechanism and  $\alpha$  a constant that in most cases equals to Boltz radius.

In order to understand the impact of top layer on field emission it is essential to bear in mind that the coating of field emitters with a thin layer of certain wide band gap materials initially enhances emission, increasing the thickness of the wide band gap material beyond a critical point, the emissivity of the coated emitter falls below that of the uncoated emitter [17,18]. Thus, the electrons can escape from the SiNx surface to a vacuum with greater ease than from the pristine CNT surface to a vacuum since the electron affinity of SiNx (~2.5 eV [19]) is lower than the work function of the CNTs ranging from 4.5 eV [20] to 5.4 eV [21].

For uncoated nanowires, Y. Chen et al., [17] have demonstrated that field emission may occur in vacuum for different CNT orientations. Extracting the data from Fig. 4 of [17] we present in Fig. 2 the Fowler-Nordheim plot which shows that for the parallel to substrate CNTs the field enhancement factor is larger.

#### 4. Results and discussion

In MIM capacitors charge injection takes place under homogeneous

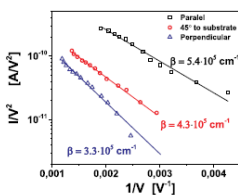


Fig. 2. F-N plot for CNTs for different orientations. Data obtained from Fig. 4 of [17].

D. Bampetis, et al.

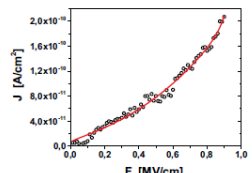


Fig. 6. Current-Voltage characteristic of Au nanorods MIM. Fitted with the sum of hopping (Eq. (2)) and Poole-Frenkel (Eq. (3)).

contains only two terms and fitted the sum to experimentally obtained I-V characteristic (Fig. 6), there it must be pointed out that the fitting results cannot be directly used to extract information on material properties but in contrary provides information on the physics on the charge transport processes in such a nanostructured dielectric film.

#### 4.3.2. MEMS capacitive switcher

Fig. 7 shows the shift of  $V_{50\%}$  during discharging of the MEMS switches with Au nanorods. In [23], a detailed comparison between the reference SiNx and nanostructured films with Au nanorods of different diameters and spacings revealed that discharging process in MEMS switches is almost 17% faster, a behavior that may be attributed to the fact that Poole-Frenkel mechanism arises from relatively low electric field intensities.

#### 4.4. Kelvin probe assessment

The decay of surface potential in a charged capacitive MEMS dielectric film and the top electrode in a charged MIM capacitor provide the information on the discharge process of dielectric film when the MEMS bridge is in up-state. In the present work the decay of surface potential is assessed with the aid of a single-point Kelvin Probe system [14], at room temperature and at ambient conditions. The method simulates the discharge process in a MEMS capacitive switch when the bridge can be still restored in the up-state. The results are presented in Fig. 8a for the random oriented CNTs and in Fig. 8b for the vertically

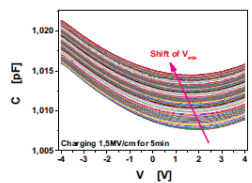


Fig. 7. Shift of  $V_{50\%}$  during discharging of the MEMS switches with Au nanorods.

Μεταπτυχιακή Διατριβή 100-101 (2019) 112360

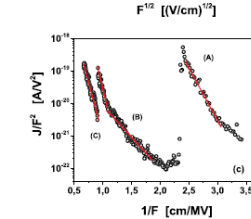
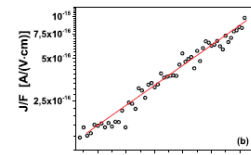
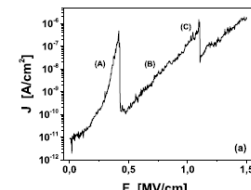


Fig. 4. (a) Ascending J-F characteristic, (b) Poole-Frenkel apparatus up to 250 V/cm and (c) F-N plot in the range of 0.25-2Mv/cm of the randomly oriented CNTs films.

electric field, generated from the parallel metal electrodes. However, the presence of nanoparticles in the lower half of dielectric films, used in this work, distorts the field homogeneity and the injected charges distribution.

In order to investigate the electrical properties of tilted nanostructured film MIM capacitors the results are separately discussed.

3

D. Bampetis, et al.

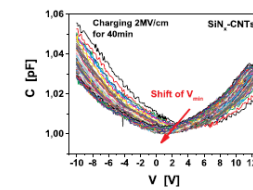


Fig. 4. Shift of  $V_{50\%}$  during discharging of the MIMs switches with randomly oriented CNTs.

#### 4.1. Assessment of randomly oriented CNTs

##### 4.1.1. MIM capacitors

Fig. 3(a) shows a typical current-voltage characteristic obtained by applying fields up to 1.5 MV/cm. The analysis of the ascending branch reveals the presence of field emission process arising from different sources. Particularly, for the ascend branch and for relatively low electric fields, up to 250 V/cm, the contribution of Poole-Frenkel mechanism appears to be dominant (Fig. 3(b)). As applied bias increases a transition to field emission is observed, where the measured current increases rapidly (Fig. 3(c)). The non-continuous behavior of the I-V process has to be attributed to breakdown and CNTs loss [22] followed by contribution from CNTs with different field enhancement factor and emitting effective area.

These results clearly show the simultaneous presence of the two charge transport mechanisms while the hopping that was not detected cannot be excluded.

##### 4.1.2. MEMS capacitive switches

Fig. 4 shows the shift of  $V_{50\%}$  during the discharging process of MEMS capacitive switches with randomly oriented CNTs. Further information about the device structure and experimental details can be found in [15]. In the later work, reference SiNx was compared with the nanostructured one and was found that MEMS switches with embedded randomly oriented CNTs provide faster discharging by an amount of 25%. The reported behavior was directly attributed to the presence of CNTs and the field-emission process, which also takes place during discharging. Furthermore, in the present work it is shown that due to the high electric fields generated by the CNTs the Poole-Frenkel mechanism also arises, thus the contribution of the combined transport mechanisms will provide faster charge draining from the bottom electrode.

##### 4.2. Assessment of vertically aligned CNTs

The current-voltage characteristics in these nanostructured dielectrics show a different behavior. Primarily, no field emission is observed (Fig. 5). The absence or very low field emission can be attributed to the electrostatic screening effect arising from the proximity of neighboring tubes and the large CNTs bunches diameter (500 nm) compared to their uniform length (100 nm) [23]. Calculations have shown that to minimize the screening effect the individual emitters should be evenly separated so that their spacing is greater than their height [24]. Furthermore, considering the fact that the nanotubes are close packed along in bunches of 500 nm diameter, which corresponds

4

Μεταπτυχιακή Διατριβή 100-101 (2019) 112360

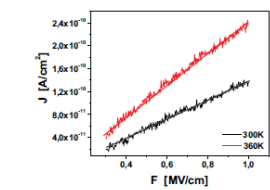


Fig. 5. Current-Voltage characteristics in MIM capacitor with vertically aligned CNT bunches for different temperatures.

to about 0.8% of the cell of dimension of  $5 \mu\text{m} \times 5 \mu\text{m}$ , we arrive to the conclusion that the measured current will arise from two regions; the empty of CNTs area and with a field intensity of 1 and the bunch top area where the field is 2k, behaving like nanowires with properties described in [25].

The current-voltage characteristic (Fig. 5) does not comply with hopping or Frenkel-Poole mechanism. Instead of this, it exhibits a very good agreement with ohm's law. Such a behavior although not encountered in silicon-nitride, it may not be excluded for the case of bulk limited conduction [26]. In the presence of even a small number of carriers excited in the conduction or valence band. Obvious like conductivity can be also obtained when hopping conduction occurs with very small hopping distance although there are no evidences of such conduction regime not observed in the case of random oriented CNTs, this behavior has to be attributed to the presence of the vertically aligned CNTs and the SiNx deposition process. The same behavior is observed at elevated temperature (360K). The fact that the conductivity increases with temperature leads to the conclusion that the mechanism is thermally activated. Based on these results we are led to the conclusion that further investigation is required for the understanding of transport mechanisms and the possible implementation on charging mitigation.

#### 4.3. Assessment of Au nanorods

##### 4.3.1. MIM capacitors

As already mentioned above, the presence of nanorods modify the distribution of the electric field intensity at the top interface of the MIM capacitors and the value of  $F_{eff}$  is determined by the height, diameter, spacing of the nanorods and the thickness of the overlying dielectric film. In [23] a comparison between nanostructured nanorods of different spacing and diameters revealed that the interference of neighboring nanorods is negligible when the spacing extends over an area of about 400-500 nm from the center of each nanorod. This condition practically means that for the nanostructured material used in the present work with diameter of 500 nm and 5  $\mu\text{m}$  spacing, the impact of the fringing field will be almost negligible. Hence, the current will be determined by both the local field intensity, the charge injection and the dominant transport mechanisms. Fig. 6 shows a typical current-voltage characteristic for electric field intensity.

In SiNx the basic transport mechanisms, in absence of any contribution from field emission, are the hopping (Eq. (2)) and Frenkel-Poole (Eq. (3)). The first is usually encountered in low fields regime while the second in high fields regime. In such a dielectric film where the electric field intensity is not uniform, we assumed that Eq. (1)

4

D. Bampetis, et al.

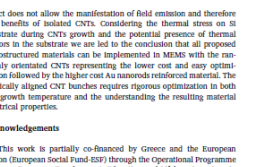


Fig. 8. Normalized surface potential decay with respect to the reference material for (a) randomly oriented CNTs, (b) vertically aligned CNTs and Au nanorods.

aligned CNTs and Au nanorods that occupy only about 0.8% of the corresponding reference materials are also included in each figure. Finally, the plots were normalized to initial potential in order to facilitate the comparison of the impact of the nanostructured material in the discharge process, which obviously increases the decay rate in these materials.

#### 5. Conclusions

The electrical properties of SiNx with embedded CNTs, of random orientation or vertically aligned bunches, and Au nanorods have been investigated. It is found that the electrical properties are significantly affected by both the embedded nanofiller and the orientation. Thus, in the case of random oriented CNTs the transport mechanisms are Frenkel-Poole and field emission at low and high electric fields respectively. Finally, for vertically aligned CNTs the current-voltage characteristics are found to follow ohm's law, leading to the conclusion that this material requires further investigation. The discharge rate through the bulk material is found to increase with the addition of nanofillers. It is important to notice that the discharge rate depends on the host material deposition conditions and the organization of the nanofillers. Furthermore, it is important to notice that for the vertically aligned nanostructures their density plays a crucial role on the discharge process. Moreover, for the vertically aligned bunches of CNTs the screening

5

Μεταπτυχιακή Διατριβή 100-101 (2019) 112360

[1] G.M. Barak, *MEMS: Theory, Design, and Technology*, J. Wiley and Sons, Hoboken, 2004.

[2] M. Koulouridis, N. Tsamir, G. Papadimitrakis, Temperature-assisted discharging process through the bulk of SiNx/SiO2 silicon nitride films for MEMS capacitive switches, *Microelectron. Reliab.* 79 (2017) 418-426.

[3] M. Koulouridis, N. Tsamir, G. Papadimitrakis, J. Papadimitrakis, Dielectric charging in capacitive microelectronic devices with silicon nitride, *Appl. Phys. Lett.* 98 (9) (2011).

[4] G. Papadimitrakis, M. S. Elbarbary, Y. Thomas, C. Wang, J. Papadimitrakis, Temperature study of the dielectric polarization effects of capacitive SiNx MEMS switches, *IEEE Trans. Electron Devices* 56 (11) (2009) 2467-2472.

[5] J. Robertson, *Intrinsic structure of silicon nitride*, *Philos. Mag.* 83 (3) (1991) 301-310 (1991) 42-7.

[6] M. Koulouridis, G. Papadimitrakis, I. Michalakis, G. Papadimitrakis, Investigation of SiNx dielectric charging, *Microelectron. Reliab.* (2018) 148-154.

[7] B. Dajic, E. Papadimitrakis, M. Koulouridis, G. Papadimitrakis, J. Papadimitrakis, Effect of deposition conditions on charging process in SiNx applications in Si-

[8] M. Koulouridis, G. Papadimitrakis, *MEMS: Theory, Design, and Technology*, 3rd ed., Wiley, Hoboken, 2014.

[9] U. Zapolnik, G. Papadimitrakis, F. Cocconi, F. Pomi, R. Pavesi, Dielectric charging in silicon nitride films for MEMS capacitive switches: effect of film thickness and deposition conditions, *Microelectron. Reliab.* 49 (1) (2009) 1309-1314 (2009/01/01).

[10] C. Rodas, et al., Carbon nanotube based dielectrics for enhanced MEMS reliability, 2007 IEEE/MTT-S International Microwave Symposium, 2007, pp. 375-378.

[11] G. Papadimitrakis, I. Michalakis, M. Koulouridis, S. Bantouras, A. Gaitis, A. Jaki, Charging mechanisms in nanostructured dielectrics for MEMS capacitive switches, 2014 IEEE 14th Topical Meeting on Silicon Nanoelectronics Integrated Circuits (SiN) System, 2014, pp. 98-100.

[12] D. Bampetis, G. Koulouridis, G. Koulouridis, G. Papadimitrakis, Temperature-assisted discharging process in SiNx films with embedded CNTs for applications in MEMS switches, 2018 Symposium on Design, Test, Integration & Packaging of MEMS and MOEMS (DTIP), 2018, pp. 1-4.

[13] X. Lu, et al., Effect of substrate and underlayer on CNT synthesis by plasma-enhanced CVD, *Adv. Mater.* 15 (1) (2003) 296-300.

[14] J. Hutter, B. Albrecht, D. Keller, On device structure variations in silicon-dioxide films, *Microelectron. Reliab.* 49 (1) (2009).

[15] M. Koulouridis, G. Koulouridis, G. Papadimitrakis, G. Koulouridis, G. Papadimitrakis, Electrical properties of SiNx films with embedded CNTs for MEMS capacitive switches, *Microelectron. Reliab.* 74-77 (2017) 614-618 (2017/06/01).

[16] J.K. Lee, Current transport and maximum dielectric strength of silicon nitride films, *J. Appl. Phys.* 86 (7) (1999) 2054-2066.

[17] Y. Chen, et al., Field emission from different oriented carbon nanotubes, *Appl. Phys. Lett.* 76 (17) (2000) 2466-2471.

[18] W. H. Wang, et al., Field-emission characteristics from single-walled carbon nanotube carbon nanotubes, *Adv. Mater.* 14 (20) (2002) 1464-1468.

[19] S. H. Park, et al., Tuning of the band structure of silicon nitride by electron affinity of hydrogen terminated Si-HxN4, Diamond and Related Materials, 13 (2004), pp. 52-57 (2004/01/01).

[20] W.S. Ho, T.C. Leung, C.T. Chen, Work function of single-walled and multiwalled carbon nanotubes, *Phys. Rev. B* 63 (23) (2001).

[21] S. Suzuki, T. Watanabe, Y. Hirohata, S. Fujihira, S. Iijima, A. Lu, et al., Work function of individual single-walled carbon nanotubes, *Appl. Phys. Lett.* 81 (1) (2004).

[22] M. Wang, M. Peng, J. Wang, G. Chen, Dielectric field emission characteristics and field evaporation of a single carbon nanotube, *J. Phys. Chem. B* 104 (1) (2000) 114-116.

[23] J. Nelson, et al., Scanning field emission from patterned carbon nanotube films, *Appl. Phys. Lett.* 76 (15) (2000) 2077-2079.

[24] D. Hwang, S. Buzza, Shielding effects in random large area field emitters, the field enhancement factor distribution, and current operation, *Phys. Plasma* 21 (2) (2018) 081005.

[25] M. Koulouridis, et al., Electrical properties of nanostructured SiNx film for MEMS capacitive switches, *J. Microelectron. Reliab.* 77 (1) (2017) 41-49.

[26] Y.-C. Chao, et al., Bandwidth and conduction mechanisms in dielectric films, *Adv. Mater. Sci. Eng.* 2 (4) (2014) 1-14.

5





3. Matroni Koutsoureli, George Stavrinidis, Dimitrios Birmpiliotis, George Konstantinidis, George Papaioannou, “Thermally Activated Discharging Mechanisms in SiNx Films with Embedded CNTs for RF MEMS Capacitive Switches” 45<sup>th</sup> International Conference on Micro& Nano Engineering, 23-26 September, 2019

### Thermally Activated Discharging Mechanisms in SiNx Films with Embedded CNTs for RF MEMS Capacitive Switches.

M. Koutsoureli<sup>1\*</sup>, G. Stavrinidis<sup>1</sup>, D. Birmpiliotis<sup>1</sup>, G. Konstantinidis<sup>2</sup> and G. Papaioannou<sup>1</sup>.

<sup>1</sup> University of Athens, Physics Department, Solid State Physics Section, 15704, Athens, Greece.  
<sup>2</sup> IESL-FORTH, GR-71110 Heraklion, Crete, Greece.

---

**ABSTRACT**

RF MEMS capacitive switches have not yet reached commercialization due to reliability problems, the most important being the effect of dielectric charging. On the way to solve this problem several approaches have been proposed up to now, among which the use of nanostructured silicon nitride (SiNx) dielectric films seems to be quite promising. In the present work nanostructured SiNx films have been fabricated, by incorporating CNTs on the lower SiNx layer. The effect of temperature on the discharging processes that take place through the bulk of these films have been probed, with the aid of a single-point Kelvin Probe (KP) system. Thermally Stimulated Depolarization Currents (TSDC) technique has been also used in order to investigate thermally activated depolarization mechanisms. The conduction mechanism that dominates charge transport in the bulk of the films has been also identified.

---

**EXPERIMENTAL DETAILS**

- MIM capacitors, of 1mm diameter, have been fabricated and tested using nanostructured SiNx films (SiNx/CNTs).
- CNTs characteristics: 1nm diameter and 2-3um length & Density in the film: 3 CNTs/10um<sup>2</sup>
- A reference SiNx material (SiNx-REF), without CNTs, has been also fabricated for comparison reasons.

**Fabrication process of SiNx/CNTs films:**

**MIM capacitor**

Figure 1: Schematic representation of a MIM capacitor.

A solution of CNTs in propanol was deposited on the bottom electrode (TiN) via spin coating.

100nm SiNx has been deposited with PECVD.

Oxygen plasma was performed with reactive ion etching.

A first layer of 100nm SiNx was grown by PECVD.

Figure 2: Top view of the samples.

---

**RESULTS**

Figure 3: Normalized values of surface potential decay during discharge measured with KP. SiNx-REF and SiNx/CNTs films measured with reference TSDC.

Figure 4: Normalized values of surface potential decay during discharge measured with KP. SiNx/CNTs films measured with reference TSDC. Influence of the characteristic time for the discharge process.

Figure 5: Hopping distance contribution present in the bulk of SiNx/CNTs and SiNx-REF film. Hopping parameters are also shown.

- A single-point KP system has been used at different temperatures (500K - 400K) in order to measure the decay of surface potential ( $U_s$ ) on the top electrode in MIM capacitors during discharge.
- The discharging process due to charge displacement through the bulk material and towards the bottom electrode has been found to be accelerated when CNTs are embedded into the lower half of SiNx film (Fig.3) and thermally activated with  $E_a=0.37$ eV (Fig.4).
- Hopping dominates charge displacement in the bulk of the films during discharge (Fig.5), since the experimental data obey the equation:
 
$$I = \frac{q_0 U_s}{d \epsilon_0} \int_0^{U_s} \frac{dU_s}{U_s} \exp\left(-\frac{U_s}{U_0}\right)$$
 where  $q_0$  is a constant proportional to mean hopping distance ( $r_{h,0}$ ),  $d$  is the film thickness and  $\epsilon_0$  corresponds to the zero field conductivity of the film.
- Larger mean hopping distance and zero field conductivity is found for nanostructured films (Fig.5).
- TSDC technique revealed that charging is lower in films with CNTs and two major contributing discharging mechanisms are found in the spectra (Fig.6).

---

**CONCLUSIONS**

- Nanostructured SiNx films with embedded CNTs have been fabricated with a simple process, in order to incorporate CNTs on the lower SiNx layer and thus assist the charge drain towards the bottom metal contact.
- The effect of temperature on the electrical characteristics of SiNx/CNTs films has been investigated, using a single-point KP system and TSDC assessment.
- The discharging process due to charge displacement in the bulk SiNx/CNTs material is found to be thermally activated and hopping dominates charge transport. Larger mean hopping distance and zero field conductivity is found for nanostructured films.
- The nanocomposite material is found to exhibit lower charging and smaller discharging time, due to field enhancement generated by the presence of CNTs.

ΕΠΙΧΕΙΡΗΣΙΑΚΟ ΠΡΟΓΡΑΜΜΑ  
ΑΝΤΑΓΩΝΙΣΤΙΚΟΤΗΤΑ  
ΕΠΙΧΕΙΡΗΜΑΤΙΚΟΤΗΤΑ  
ΚΑΙΝΟΤΟΜΙΑ

ΕΥΡΩΠΑΪΚΟ ΤΑΜΕΙΟ  
ΑΝΑΠΤΥΞΗΣ ΚΑΙ  
ΚΟΙΝΩΝΙΚΗΣ ΔΙΚΑΙΟΔΟΣΙΑΣ

\*matroni@physics.uoi.gr



ΕΛΛΗΝΙΚΗ ΔΗΜΟΚΡΑΤΙΑ  
ΥΠΟΥΡΓΕΙΟ  
ΟΙΚΟΝΟΜΙΑΣ & ΑΝΑΠΤΥΞΗΣ  
ΕΙΔΙΚΗ ΓΡΑΜΜΑΤΕΙΑ ΕΠΑ & ΤΕ  
ΕΙΔΙΚΗ ΥΠΗΡΕΣΙΑ ΔΙΑΧΕΙΡΙΣΗΣ ΕΠΑ-ΕΚ

ΕΠΑ-ΕΚ 2014-2020  
ΕΠΙΧΕΙΡΗΣΙΑΚΟ ΠΡΟΓΡΑΜΜΑ  
ΑΝΤΑΓΩΝΙΣΤΙΚΟΤΗΤΑ  
ΕΠΙΧΕΙΡΗΜΑΤΙΚΟΤΗΤΑ  
ΚΑΙΝΟΤΟΜΙΑ

ΕΣΠΑ  
2014-2020  
ανάπτυξη - εργασία - αλληλεγγύη

Με τη συγχρηματοδότηση της Ελλάδας και της Ευρωπαϊκής Ένωσης



## 4. Παρουσιάσεις σε διεθνείς επιστημονικές συναντήσεις

1. Συμμετοχή στο 40th TCM CapTech Meeting, 17 Σεπτεμβρίου 2018, EDA, Βρυξέλλες, Παρουσίαση του PANTAP

40<sup>th</sup> TCM CapTech Meeting  
September 17 2018; EDA, Brussels, BE

**Hellenic Naval Academy (HNA) – Ministry of National Defense – Hellenic Republic**

*Evangelia Karagianni*  
Associate Professor  
Electronics Laboratory Director  
Hellenic Naval Academy

ΣΧΟΛΗ ΝΑΥΤΙΚΩΝ ΔΟΚΙΜΩΝ  
HELLENIC NAVAL ACADEMY www.hna.gr

FORTH  
INSTITUTE OF ELECTRONIC STRUCTURE AND LASER

**HNA Projects**  
RADAR: "Heterogeneous Three-dimensional Integration using innovative nanotechnologies for the new generation of microwave power transceivers"

- EU, Action for Research, "Research - Creation - Innovation", 2014-2020, Operational Program "Competitiveness-Entrepreneurship-Innovation"
- The project has three years duration (from now), seven institutions cooperate and the coordinator is the Foundation for Research and Technology (FORTH) of Crete.
- The Hellenic Naval Academy as a partner has to design and optimize the GaN MMIC circuits: the High Power Amplifier (HPA) and the Low Noise Amplifier (LNA).
- Modeling will be done using electronic design and electromagnetic simulation software such as ADS, HFSS, EMPro and CST to achieve optimization of performance. The design library will be completed in parts through a sufficient number of feedback cycles to design, construct, characterize and compare between computational and experimental data.

MEMS membrane  
GaN/SiC substrate ~500µm  
Metallic Platform

**RADAR : MRG/FORTH Innovation: COPLANAR Approximation (CP)**

**Coplanar Approximation (IESL Innovation)**

MEMS membrane  
GaN/SiC substrate ~500µm

1. HPA & LNA Construction (20 fabrication steps)
2. RF MEMS & Air-Bridge Interconnect (7 fabrication steps)

**Total fabrication steps : 27**  
Without backside processing  
Higher yield & better circuits  
mechanical reliability

**Microstrip Approximation (Existing Technology)**

MEMS membrane  
Thin GaN/SiC substrate  
Substrate thinning process

1. HPA & LNA Construction (20 fabrication steps)
2. substrate thinning (3 high "risk" and cost fabrication steps)
3. Vias & Au metal Via filling process (6 high "risk" and cost fabrication steps)
4. RF MEMS & Air-Bridge Interconnect (7 fabrication steps)

**Total fabrication steps : 36**

**RADAR**

- The 1<sup>st</sup> Prototype (GaN/SiC) will operate at X-band (8-12 GHz) with 50W output power
  - The T/R of this category mainly targets the purchase of radar for ground-based marine and flying applications.
  - This market requires a relatively small number of T/R (~ 1000 / radar), but the cost of each can be as high as 200-300 €, it is a performance driven application.
- The 2<sup>nd</sup> Prototype (GaN/Si) will operate at Ka-band (26-40 GHz) with 30W output power and with the required thermal support.
  - **HPA:** The current state of commercial products is that high output power (over 40W) is provided in zones L and C. In zone X the highest power value is 35W while in zone Ka the value is 15W. In general, commercial products are produced using the microstrip technique.
    - "RADAR": The first HPAs will be produced with coplanar technique with 50W power in X-band and 30W in Ka Band.
  - **LNA:** Commercial products are up to 22GHz with a noise figure (NF) of 2.5dB.
    - "RADAR": Coplanar LNA with SotA performance will be produced. The emphasis will be on the Ka band targeting NF <1.7dB.



Ευρωπαϊκή Ένωση  
Ευρωπαϊκό Ταμείο  
Περιφερειακής Ανάπτυξης



Με τη συγχρηματοδότηση της Ελλάδας και της Ευρωπαϊκής Ένωσης

ΕΠΑνεΚ 2014-2020  
ΕΠΙΧΕΙΡΗΣΙΑΚΟ ΠΡΟΓΡΑΜΜΑ  
ΑΝΤΑΓΩΝΙΣΤΙΚΟΤΗΤΑ  
ΕΠΙΧΕΙΡΗΜΑΤΙΚΟΤΗΤΑ  
ΚΑΙΝΟΤΟΜΙΑ



ανάπτυξη - εργασία - αλληλεγγύη



## 5. Προβολή σε Μέσα Κοινωνικής Δικτύωσης και Εταιρικές Ιστοσελίδες

### 1. Προβολή του έργου και της εξέλιξης του στα Μέσα Κοινωνικής Δικτύωσης.

**Prisma Electronics**  
@prismaelectronics

Αρχική σελίδα  
Πληροφορίες  
Φωτογραφίες  
Κριτικές  
Βίντεο  
Δημοσιεύσεις  
Εκδηλώσεις  
Κοινότητα  
**Δημιουργήστε Σελίδα**

👍 Σας αρέσει ▾ 📡 Ακολουθείτε ▾ ➦ Κοινοποίηση

📍 Ο χρήστης Prisma Electronics πρόσθεσε 4 νέες φωτογραφίες από 19 Απριλίου 2019 — στην πόλη Αλεξανδρούπολη.  
19 Απριλίου 2019 · 🌐 · 📍

The Radar project consortium held its second plenary meeting in Alexandroupolis, on the day 18 of April 2019.

The meeting took place in the industrial unit of Prisma Electronics in the Industrial Area of Alexandroupolis

The partners presented the main outcomes from the work packages so far, discussed and agreed on the most relevant questions, worked together on specific topics, made a general assessment of the project and planned the next steps.

More info for Radar project at <https://radar-project.iesl.forth.gr/>  
Δείτε τη μετάφραση

📍 Democratas Avenue 87  
68132 Αλεξανδρούπολη  
Λήμη οδηγίων  
📞 2551 035013  
✉️ Αποστολή μηνύματος  
🌐 [www.prismaelectronics.eu](http://www.prismaelectronics.eu)  
📁 Κατάστημα υπολογιστών · Βιομηχανική εταιρεία  
📄 Impressum  
✍️ Προτείνετε διορθώσεις

📍 5,0 5 στα 5 · Βάσει της άποψης 8 απόμων

**Κοινότητα** Προβολή όλων

👤 Προσκαλέστε τους φίλους σας να δηλώσουν ότι τους αρέσει αυτή η Σελίδα

👍 Αρέσει σε 2.859 άτομα  
📡 2.880 άτομα ακολουθούν αυτή τη Σελίδα.

👤 Ιορδάνης Νηδελης and 13 ακόμη φίλοι like this or have checked in

👤 45 κοινοποιήσεις παρουσίας

**Πληροφορίες** Προβολή όλων

📍 Democratas Avenue 87  
68132 Αλεξανδρούπολη  
Λήμη οδηγίων  
📞 2551 035013  
✉️ Αποστολή μηνύματος  
🌐 [www.prismaelectronics.eu](http://www.prismaelectronics.eu)  
📁 Κατάστημα υπολογιστών · Βιομηχανική εταιρεία  
📄 Impressum  
✍️ Προτείνετε διορθώσεις

📘 Διαφάνεια Σελίδας Δείτε περισσότερα

Το Facebook δείχνει πληροφορίες για να σας βοηθήσει να κατανοήσετε καλύτερα ποιος είναι ο σκοπός μιας Σελίδας. Δείτε τις ενέργειες που κάνουν τα άτομα που διαχειρίζονται και δημοσιεύουν περιεχόμενο.

📅 Ημερομηνία δημιουργίας Σελίδας - 6 Απριλίου 2011

🔗 Σχετικές Σελίδες

👍 Εσείς και 5 ακόμη

👍 Μου αρέσει! 🗨️ Σχόλιο ➦ Κοινοποιήστε



ΕΛΛΗΝΙΚΗ ΔΗΜΟΚΡΑΤΙΑ  
ΥΠΟΥΡΓΕΙΟ  
ΟΙΚΟΝΟΜΙΑΣ & ΑΝΑΠΤΥΞΗΣ  
ΕΙΔΙΚΗ ΓΡΑΜΜΑΤΕΙΑ ΕΠΙΧΕΙΡΗΣΙΑΚΩΝ  
ΕΠΙΧΕΙΡΗΣΙΑΚΩΝ ΠΡΟΓΡΑΜΜΑΤΩΝ

ΕΠΙΧΕΙΡΗΣΙΑΚΟ ΠΡΟΓΡΑΜΜΑ  
ΑΝΤΑΓΩΝΙΣΤΙΚΟΤΗΤΑ  
ΕΠΙΧΕΙΡΗΜΑΤΙΚΟΤΗΤΑ  
ΚΑΙΝΟΤΟΜΙΑ

ΕΣΠΑ  
2014-2020  
ανάπτυξη - εργασία - αλληλεγγύη

Με τη συγχρηματοδότηση της Ελλάδας και της Ευρωπαϊκής Ένωσης



Σελίδα Κέντρ... Εισερχ... Creator Studio Manage Jobs Ειδοπ... Περισσότερα... Επεξε... Ρυθμί... Βοήθεια



**Prisma Electronics**  
@prismaelectronics

- Αρχική σελίδα
- Πληροφορίες
- Φωτογραφίες
- Κριτικές
- Βίντεο
- Δημοσιεύσεις**
- Εκδηλώσεις
- Υπηρεσίες
- Κατάστημα
- Ομάδες
- Σημειώσεις
- Προσφορές
- Θέσεις εργασίας
- Κοινότητα

**Προώθηση**

Επισκεφτείτε το Κέντρο Διαφημίσεων

Σας αρέσει... Ακολουθήστε... Κοινοποίηση... Επικοινωνήστε μαζί μας

**Prisma Electronics**  
Δημοσιεύτηκε από Dimosthenis Karageorgiou (·) · 29 Νοεμβρίου 2019 · 🌐 · 🌐

The Radar project consortium held its third plenary meeting in Athens, on the day 29 of November 2019.

The meeting took place in the Institute of Nano science and Nanotechnology at Athens.

The partners presented the main outcomes from the work packages so far, discussed and agreed on the most relevant questions, worked together on specific topics, made a general assessment of the project and planned the next steps.

More info for Radar project at <https://radar-project.iesl.forth.gr/>



**Προώθηση Δημοσίευσης**

👍 Εσείς και 2 ακόμη

👍 Μου αρέσει! 💬 Σχόλιο ➦ Κοινοποιήστε 🌐

Αναζητήστε δημοσιεύσεις σε αυτή τη :

Ελληνικά · English (US) · Shqip · Español · Português (Brasil)

Πληροφορίες για τα στατιστικά στοιχεία Σελίδων  
Απόρρητο · Όροι · Διαφήμιση · Οι Διαφημίσεις μου >

· Cookies · Περισσότερα · Facebook © 2020

<https://www.prismaelectronics.eu/index.php/en/profil-en/news-en/186-the-radar-project-consortium-held-its-third-plenary-meeting-in-athens>


HOME / PROFILE / NEWS / THE RADAR PROJECT CONSORTIUM HELD ITS THIRD PLENARY MEETING IN ATHENS

**Prisma Electronics**

HOME PROFILE ACTIVITIES SOLUTIONS CONTACT 🔍 Search

## The Radar project consortium held its third plenary meeting in Athens

WRITTEN ON 29 NOVEMBER 2019



The Radar project consortium held its third plenary meeting in Athens, on the day 29 of November 2019.

The meeting took place in the Institute of Nano science and Nanotechnology at Athens.

The partners presented the main outcomes from the work packages so far, discussed and agreed on the most relevant questions, worked together on specific topics, made a general assessment of the project and planned the next steps.

More info for Radar project at <https://radar-project.iesl.forth.gr/>